

IN THE SPECIFICATION

Please replace paragraph [0038] with the following amended paragraph:

{0038] The operation of the BIST circuit of Figure 4 is similar to that of [[Figure2]] Figure 2 with the difference being that the on-chip processor performs the functionality of and replaces the external test controller and frequency counter. For the case of an on-chip software based processor (e.g., microprocessor or DSP) used to perform the test, the testing software is adapted to configure, control and interface with the RF modulator, configurable threshold register and configurable division register. At the conclusion of a test, the processor is adapted, under software control, to read the test out signal output of the frequency divider.